

FIG.1A

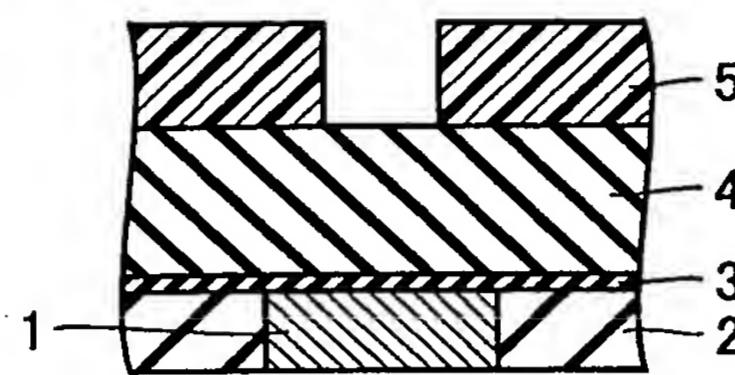


FIG.1B

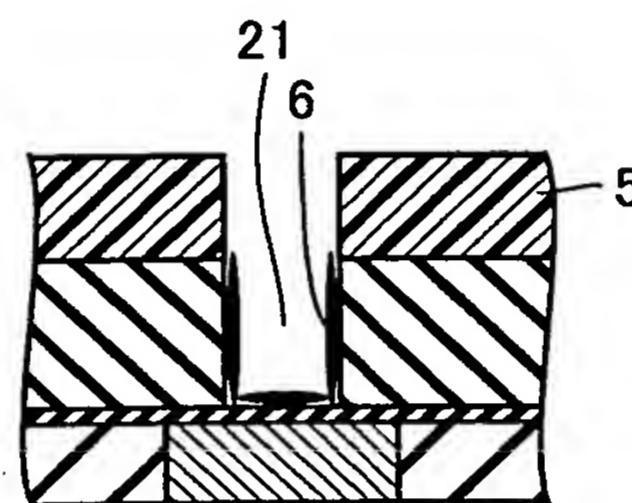


FIG.1C

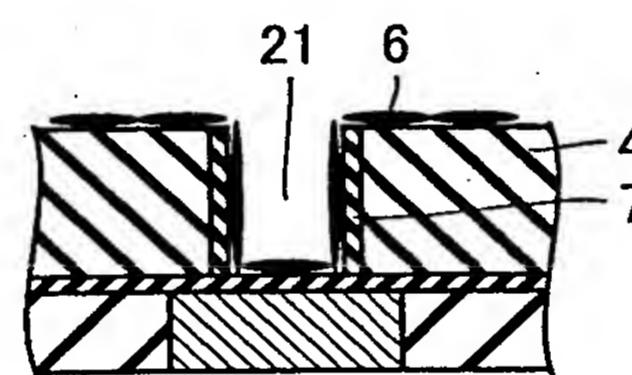
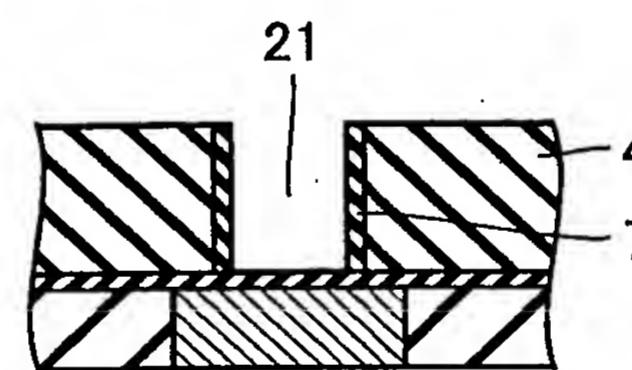


FIG.1D



Title: CLEANING COMPOSITION FOR REMOVING RESISTS AND MANUFACTURING  
METHOD OF SEMICONDUCTOR DEVICE

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Atty Docket No.: 402696  
Leydig, Voit & Mayer 202-737-6770

FIG.1E

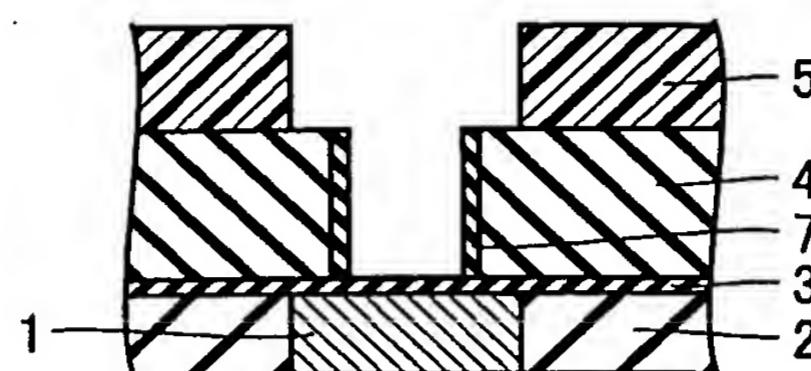


FIG.1F

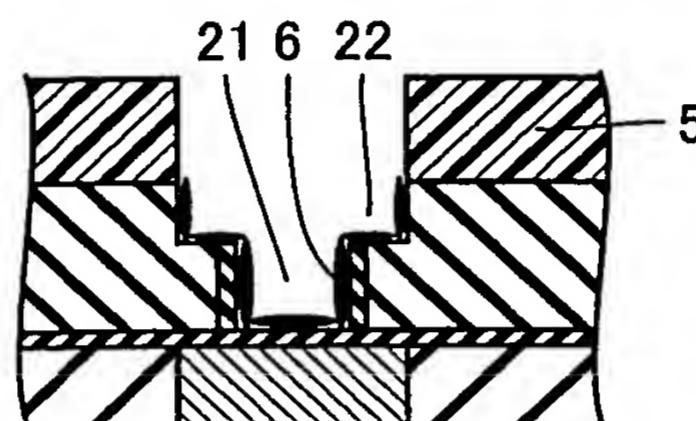


FIG.1G

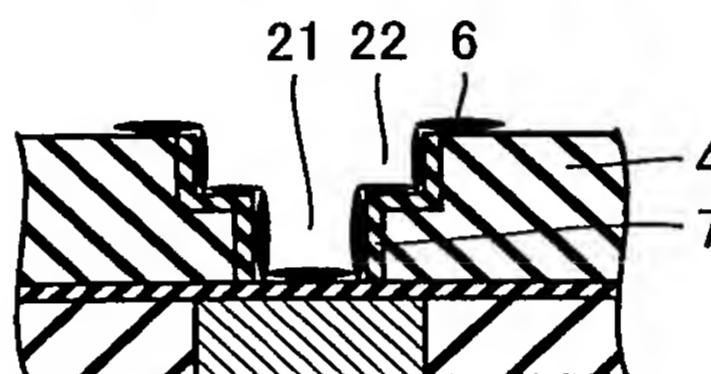


FIG.1H

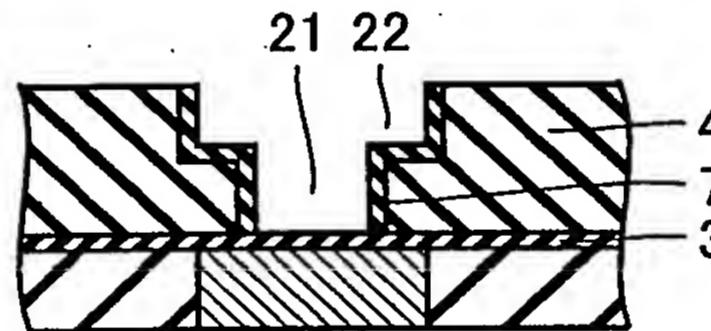


FIG.1I

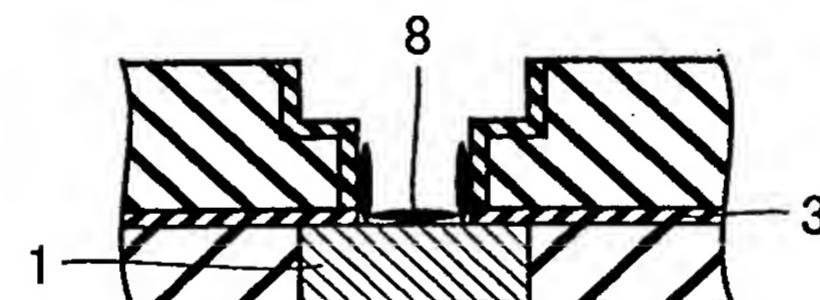


FIG.1J

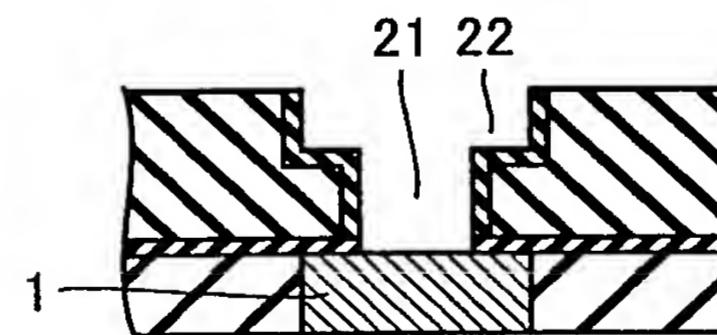


FIG.1K

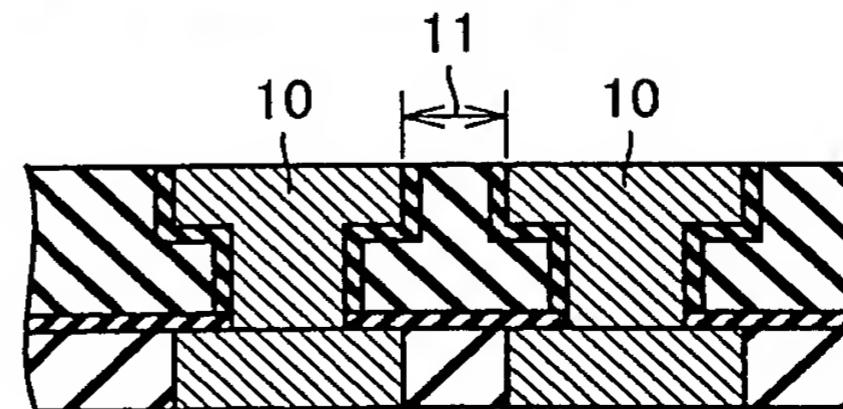


FIG.2A

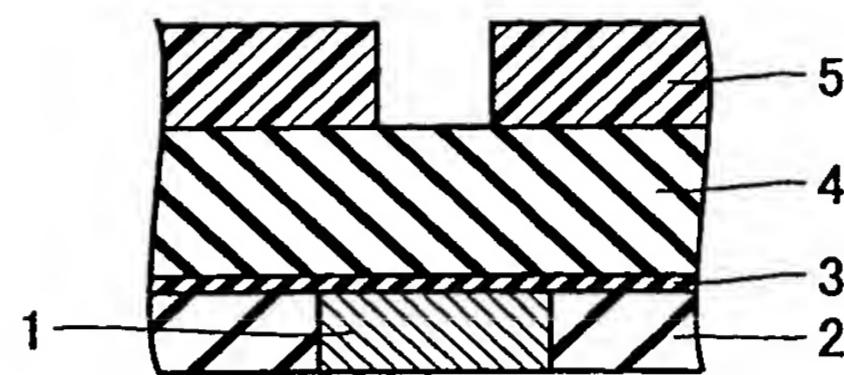


FIG.2B

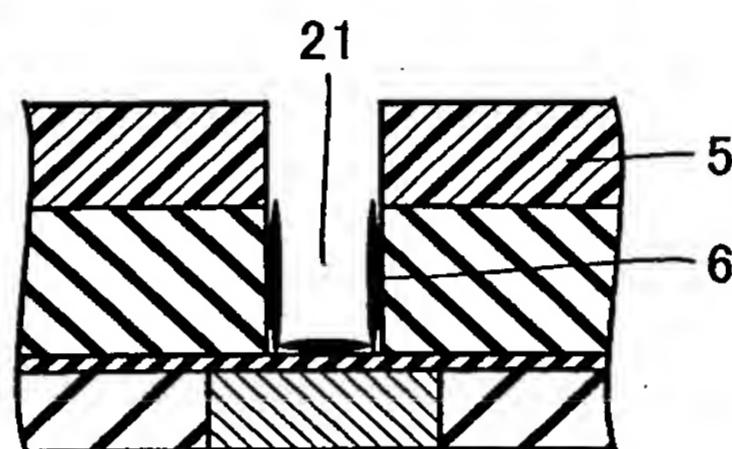


FIG.2C

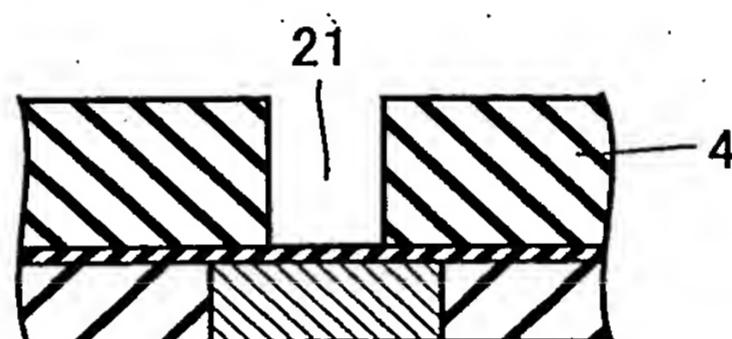


FIG.2D

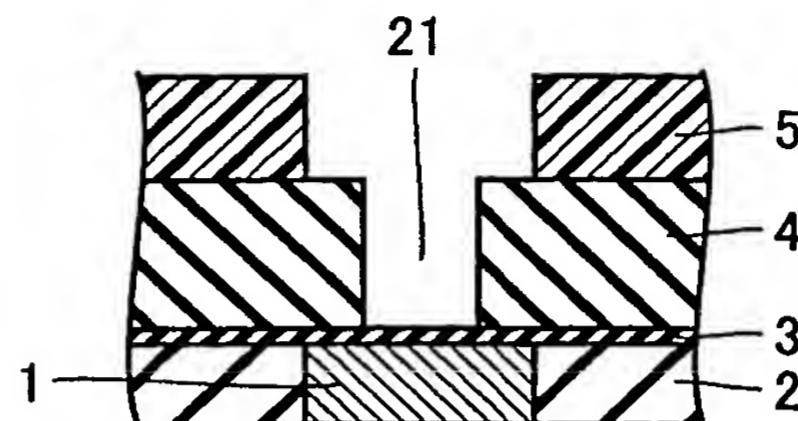


FIG.2E

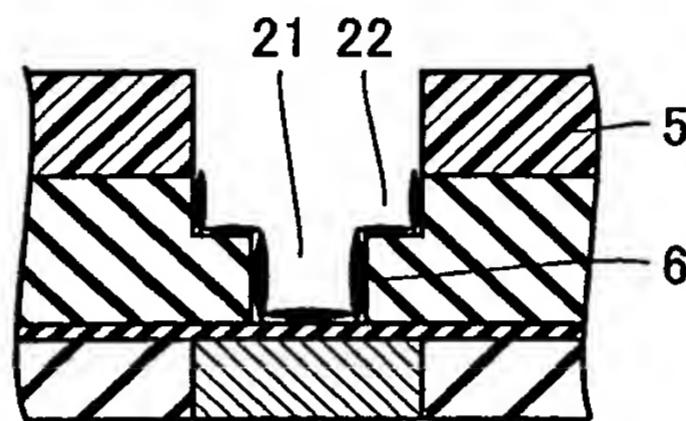


FIG.2F

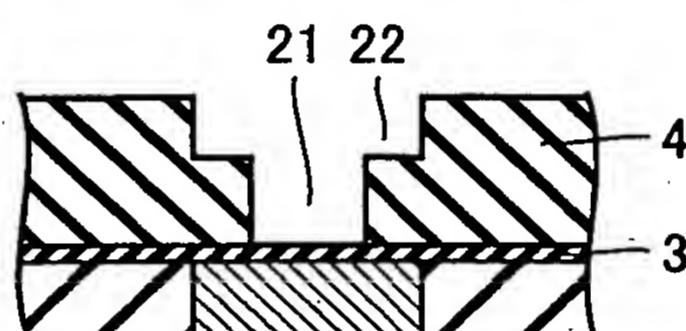


FIG.2G

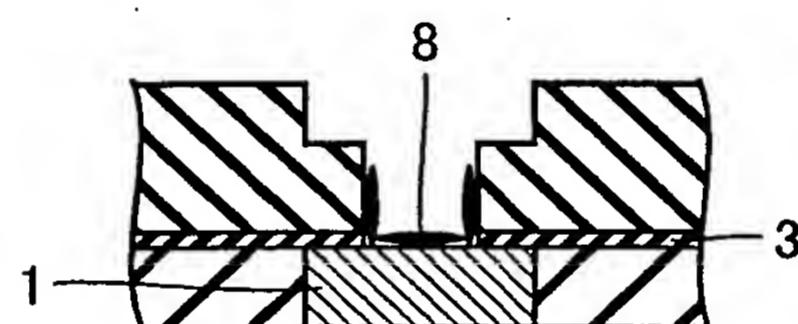


FIG.2H

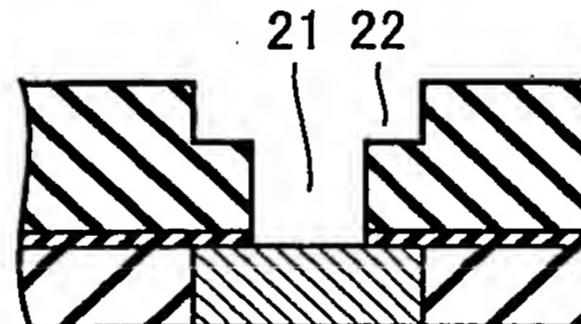


FIG.2I

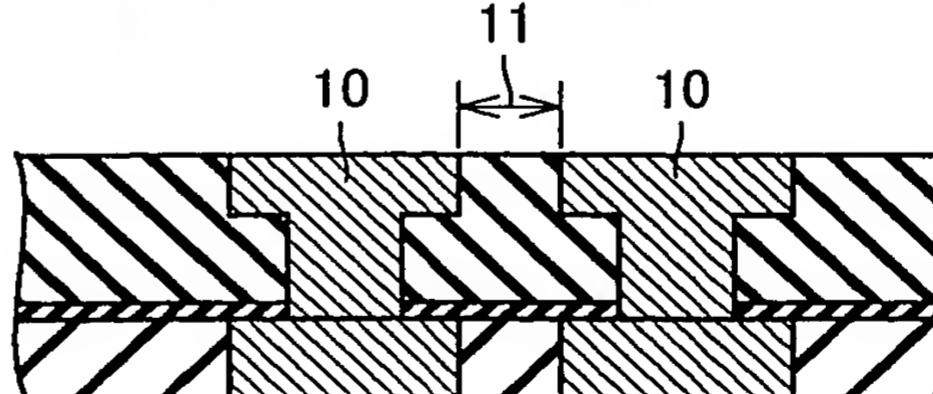


FIG.3A  
PRIOR ART

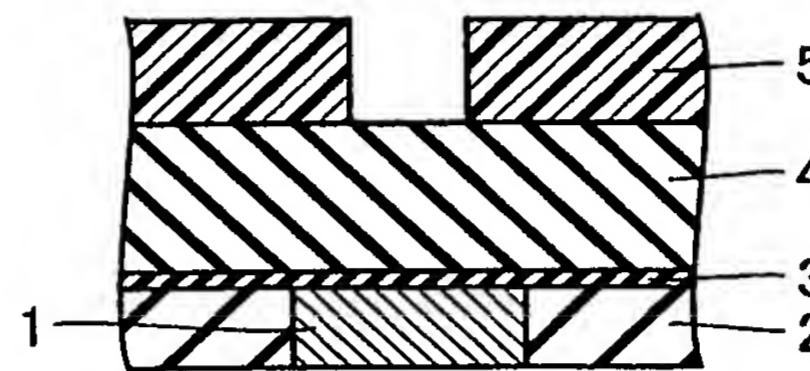


FIG.3B  
PRIOR ART

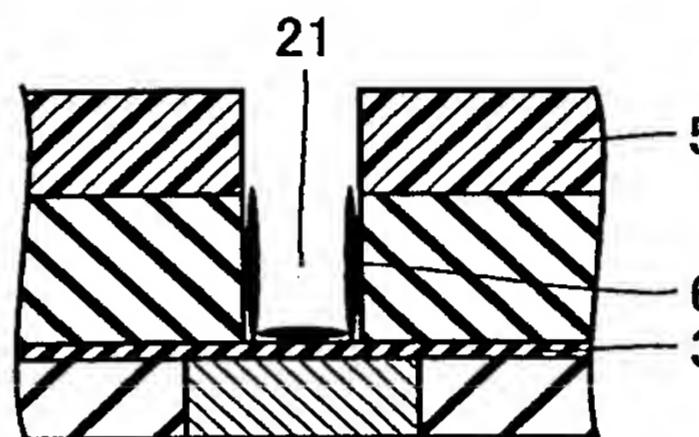


FIG.3C  
PRIOR ART

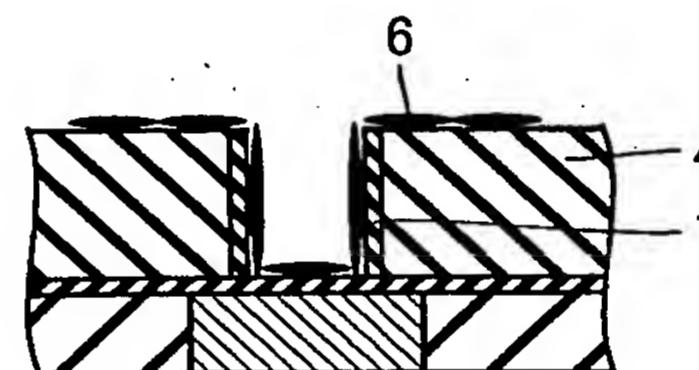
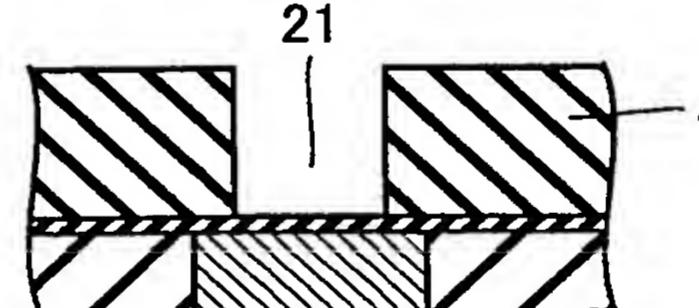
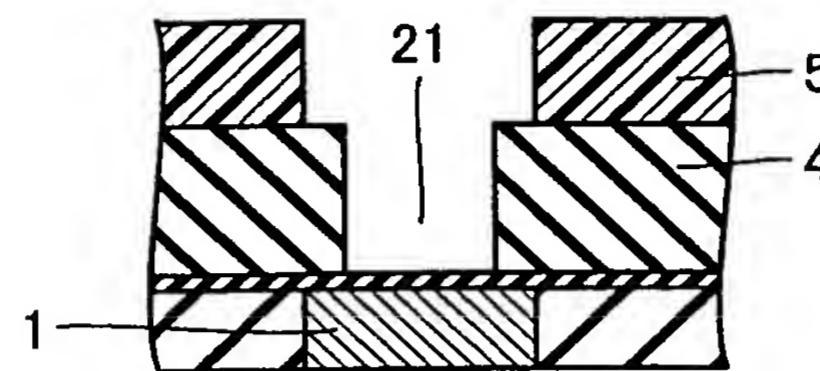


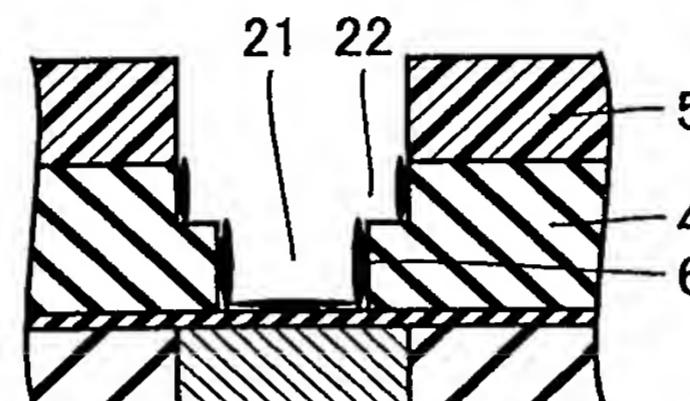
FIG.3D  
PRIOR ART



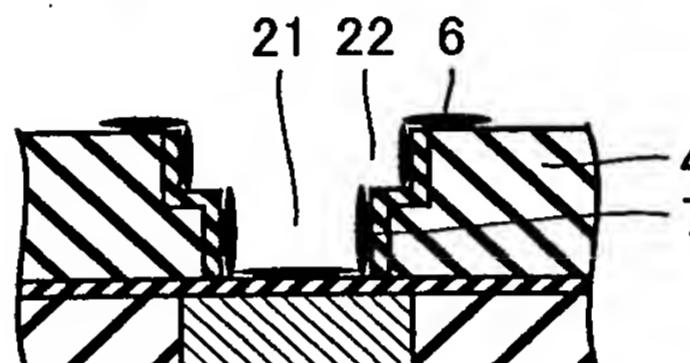
**FIG.3E**  
PRIOR ART



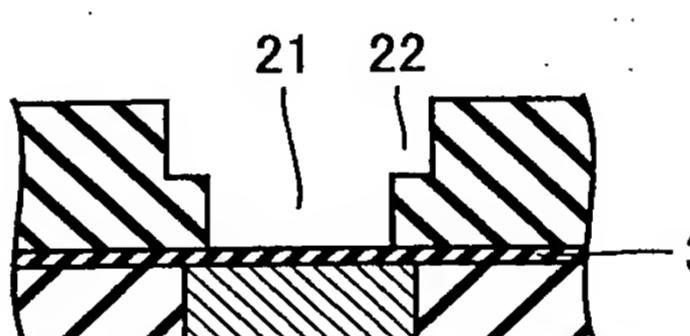
**FIG.3F**  
PRIOR ART



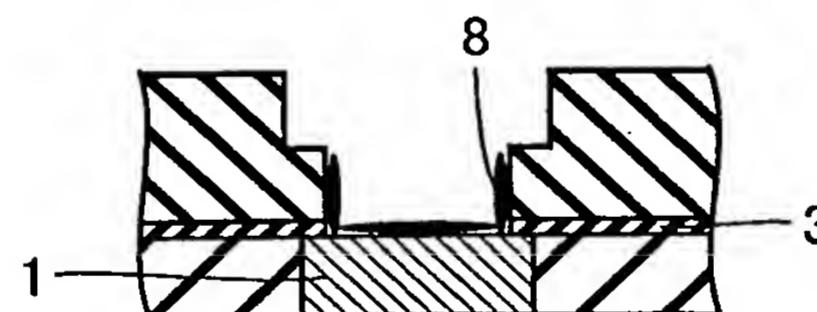
**FIG.3G**  
PRIOR ART



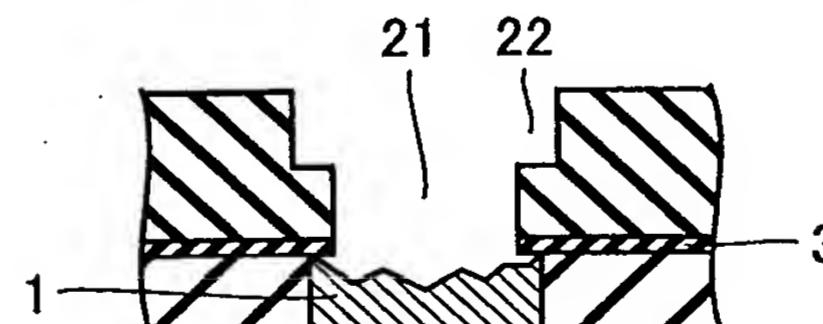
**FIG.3H**  
PRIOR ART



**FIG.3I**  
PRIOR ART



**FIG.3J**  
PRIOR ART



**FIG.3K**  
PRIOR ART

